237213@gulfjobseekers.com

**SUMMARY**

* An Electronics & Communication Engineer graduated from PDM

College Of Engineering and having knowledge of Embedded & VLSI Technologies.

* Self motivated with strong communication skills, an ability & hungry to learn new technologies, methodologies, strategies and processes quickly and [work](http://www.careerride.com/sample-resume-for-fresher-engineers.aspx) as a team.

**CAREER OBJECTIVE**

Quest to work in a professional atmosphere with a company that seeks a career conscious person where my acquired skills & knowledge will be utilized towards continuous growth & development of the organization.

**EDUCATION DETAIL**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | Level |  | College , University | Year |  | Percentage |  |  |
|  | **PG-DIVESD (*Post Graduate -*** |  |  |  |  | 66.9 % |  |  |
|  | ***Diploma in Integrated VLSI &*** |  | CDAC , Noida | 2015 |  |  |  |
|  |  |  |  |  |  |
|  | ***Embedded Systems Design*)** |  |  |  |  |  |  |  |
|  | **B.TECH *(Electronics &*** |  | P.D.M. College Of Engg, |  |  |  |  |  |
|  |  | Bahadurgarh, Haryana | 2013 |  | 60.87 % |  |  |
|  | ***Communication)*** |  |  |  |  |
|  |  | *(M.D.U. Rohtak)* |  |  |  |  |  |
|  |  |  |  |  |  |  |  |
|  | **12th** |  | St. Columba’s School, | 2009 |  | 71.8 % |  |  |
|  | **(*PCM with ED*)** |  | New Delhi *(CBSE)* |  |  |  |
|  |  |  |  |  |  |  |
|  | **10th** |  | St. Columba’s School, | 2007 |  | 75.2 % |  |  |
|  |  | New Delhi *(CBSE)* |  |  |  |
|  |  |  |  |  |  |  |  |

**TRAININGS & CERTIFICATIONS**

* Four months Training at TICO Institute of Embedded Technology.
* Six weeks Summer Training at HCL Infosystems, Noida in Networking & Technology Devices.
* Six weeks Summer Training at LD&T Division, Central Electricity Authority, New Delhi in Optical Fibre Communication.

**TECHNICAL SKILLS**

* **HDL Language:** Verilog, VHDL
* **Embedded Technology:** 8051, AVR, ARM
* **EDA Tools:** ModelSIM SE, Keil uVision, 8051 IDE
* **Programming Languages:** C, C++
* **Operating Systems:** Windows, Red Hat Linux 5.0
* **Networking** (CCNA)
* **Others:** Microsoft Office, Internet Surfing

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Curriculum Vitae

**PROJECTS DETAIL**

**Title:** HDLC PROTOCOL **Platform:** VLSI Design

**Role Played:** Programmer and Tester

**Description:** ‘HIGH-LEVEL DATA LINK CONTROL’ (HDLC) PROTOCOL using VERILOGis used to send the data in the form of frames, a controller controls the flow of data in DATA LINK LAYER of OSI model. HDLC protocol is used to transmit frames in logic link layer of Data link Layer. HDLC frame consists of an 8 bit Flag bit followed by control bits, information bits, FCS bits (CRC), address bits & terminates with flag bit. It involves processing of data before transmission termed as Zero Stuffing which is special feature of HDLC.

**Title:** MULTI-CHANNEL MONITORING & DATA LOGGER **Platform:** Embedded System Design

**Role Played:** Designer, Programmer and Tester

**Description:** 'MULTI-CHANNEL MONITORING & DATA LOGGER' is a device based onAtmel AT89S52 used for various applications such as environmental/health monitoring & measurements include temperature, light, voltage, smoke etc. In my project the analog signals were converted & then were displayed by either through LCD or PC. I had used ADC0809, MAX232, Sensors [Light (LDR), Temperature (LM35), POT], LCD, KEYs, LEDs, Relays, Buzzer, DB9 cable etc. The after effects were shown via FAN, BULB.

**Title:** iROB

**Platform:** Embedded System Design

**Role Played:** Designer, Programmer and Tester

**Description:** 'iROB' is a collective name used for multipurpose robot based on AtmelAT89S52 used for various applications such as line follower, obstruction avoidance, wireless controlling etc. In my robot the signals were detected through sensors like IR, Ultrasonic, and RF (433MHz). I had used ICs such as Enc/Dec, L293D etc.

**STRENGTHS**

* Good oral, written & presentation skills
* Creative, Self Motivated & Quick learner
* Leadership Qualities
* Adjustable to any Environment

**EXTRA CURRICULAR**

* Certificate of Organiser of EXTREME GAMERS conducted at PARADIGM’13 held at

P.D.M. College of Engg.

* Certificate of Participation in ISSET conducted by IEEE held at Delhi Technological University.
* Certificate of Participation on Ethical Hacking & Cyber Forensic conducted by Delhi Software Solution held at P.D.M. College of Engg.
* Certificate of Merit for securing 1st position in Logo Designing at COLUMBAN FEST.
* Certificate of Honour for securing 2nd position in Street Play at SYMSCI’06.
* Certificate of Merit for securing 2nd position in Flower Arrangement at PRISM’04.

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Curriculum Vitae

**HOBBIES**

* Designing
* Organising Events
* Photography
* Travelling, meeting and interacting with new people.
* Playing football, badminton, swimming & cycling.
* Acting, Listening music & watching movies.

**PERSONAL INFORMATION**

* **Date of Birth :** 12th April 1991
* **Gender & Status :** Male & Single
* **Nationality :** Indian
* **Languages :** English, Hindi and Malayalam

I hereby declare that all the information given above is true to the best of my knowledge.

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