EVELYN

[EVELYN.332520@2freemail.com](mailto:EVELYN.332520@2freemail.com)

**CAREER OBJECTIVE**:

To be associated with progressive organization that gives me scope to update my skills according to latest trends, especially in the area of Software Testing and be a part of the team that dynamically works towards the growth of organization.

**SCHOLASTIC PROFILE:**

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| --- | --- | --- | --- |
| **COURSE** | **INSTITUTIONS** | **YEAR OF PASSING** | **PERCENTAGE%** |
| B.E  (ECE) | Tejaa Shakthi Institute of Technology ,Coimbatore | 2016 | 83% |
| HSC  (State board) | St’Thomas Higher Secondary School,Gudalur | 2012 | 70% |
| SSLC  (matriculation) | Holy Cross Convent Matriculation Higher Secondary School,Gudalur | 2010 | 81.2% |

**CERTIFICATION:**

* Completed training in CADPOINT(IIHT) institute on Software Testing.

**SOFTWARE PROFICIENCY:**

TESTING **:** Manual testing,Compatibility testing.

LANGUAGE **:** C(basic)

OPERATING SYSTEM **:** Windows family(XP,7,8)

WEB DESIGNING **:** HTML5

PACKAGE **:** Photoshop, Microsoft Office, Xilinx

AREA OF INTEREST **:** Electronic devices, Manual testing

**RESPONSIBILITIES*:***

* Preparation of test plans and test cases.
* Ability to write test cases and test scenarios.
* Understanding of software development system process.
* Understanding of software testing life cycle and bug life cycle.
* Familiar with functional and non-functional testing.
* Well versed with Test scenarios, Test cases, Test execution.
* KnowledgeeindCompatibity,Responsiveness,Functionl,Regression,Black box,UI,GUI,Smoke and Alpha testing.
* Knowledge in Black box testing techniques like BVA, ECP and Use case testing.
* Knowledge to test Web applications

**ACHIEVEMENTS:**

* Got centum in “**DIGITAL SIGNAL PROCESSING , DIGITAL IMAGE PROCESSING AND TPDE”.**
* Selected in “**EETP TRAINING** “conducted by BHARAT SANCHAR NIGAM LIMITED ,Coimbatore.
* Class topper in the academics for all semester.

**CO CURRICULAR ACTIVITIES:**

**Paper Presented**

|  |  |  |
| --- | --- | --- |
| Topic | Conference/Symposium | Institute/Industry |
| Automated Multistored Car Parking System In The Building | Eclectic 2k14 | Kalaivani College Of Technology |
| Artificial Vision Using Embedded System | ECLAT’15 | Erode Sengunthar Engineering College |

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**ACADEMIC PROJECT:**

**TITLE: Design and simulation of area and delay efficient VLSI architecture by AES algorithm using CADENCE TOOL.**

* The basic theme of this project is to implement a area and delay efficient AES algorithm using key expansion approach. AES algorithm has been widely deployed in cryptographic application. We minimize the area utilization and critical path delay using the proposed high performance architecture. It supports both encryption and decryption using 256-bit keys with a throughput of 0.06Gbps

**EXTRA CURRICULAR ACTIVITIES:**

* **CLASS REPRESENTATIVE , PROJECT and CULTURAL CO-ORDINATOR**

**-**ECE dept - Tejaa Shakthi Institute of Technology for Women

**PERSONAL DETAILS:**

**DOB :** 12.09.1994

**Linguistic Ability :**English , Tamil, Malayalam

**DECLARATION:**

I hereby confirm that the information furnished above is true and correct as per my knowledge.