**Madeeha**

**Madeeha.364168@2freemail.com**

 

Objective

##### Intend to build a career with leading corporative of hi-tech environment with committed and dedicated people, which will help me to explore myself fully and realize my potential. Willing to work as a key player in challenging and creative environment

Professional Summary

##### **2+ years** of experience in testing projects, Good Knowledge of Testing Practices, Quality Control

##### Extensive experience in Manual Testing, Functional Testing & Web Based Testing

##### Good Knowledge of software testing with good exposure in testing applications

* Experience in HR\_ Management Software
* Worked on HR Management Services application for Leading UAE clients
* Strong experience in software testing process including executing tests, record result, and test reports and tracking defects.

##### Involved in regression testing, functional testing, development of test cases, executing test cases

* Good Testing Experience in Test Complete and Rational Quality Management (IBM)
* Experience of close analysis & test case preparation/business rule modifications
* Lead team of 6 and successfully completed the scheduled testing.

Technical Skills

* Programming Languages : .NET, VHDL
* Application Software’s : MATLAB, XILLINX (VHDL),VISUAL STUDIO 2010,ORCAD, MICROWIND, MODEL SIM, QUARTUS II,IBM-RDNG,TEST COMPLETE
* Database : SQL Server
* Office Tools : MS-Word, MS-Excel, MS-PowerPoint
* Operating Systems: Windows 10/08/07, Vista Basic, Home
* Test Automation Tool : TestComplete

Professional Work History

[**DLI-IT Systems Group**](https://www.linkedin.com/company/dli-systems-group---employee-time-attendance-payroll-%26-personnel-management-system?actionToken=p%3Dp%253Dbiz-company-public%2526c%253D795513f1-cb5b-4022-8ce3-260cef213de6%2526m%253Dcompany_feed%2526n%253D0%26t%3Da%253DisFolloweeOfPoster%25253Dfalse%252526distanceFromActor%25253D-1%252526actorType%25253D%252526likedByUser%25253Dfalse%252526targetId%25253D%252526recentCommentUrns%25253D%252526targetType%25253D%252526sponsoredFlag%25253DORGANIC%252526verbType%25253Dlinkedin%2525253Ashare%252526objectType%25253Dlinkedin%2525253Acontent%252526totalShares%25253D0%252526activityId%25253Durn%2525253Ali%2525253Aactivity%2525253A6179622770167648256%252526recentLikerUrns%25253D%252526actorId%25253Durn%2525253Ali%2525253Acompany%2525253A3008399%252526totalComments%25253D0%252526relevanceScore%25253D0%2E0%252526recentCommenterUrns%25253D%252526isPublic%25253Dtrue%252526time%25253D-1%252526totalLikes%25253D0%252526objectId%25253Durn%2525253Ali%2525253Acontent%2525253AJPG%2525252FIMG%2525252F51147e9577b845db9568e0d3c9954a0d%252526distanceFromNestedActor%25253D-1%2526s%253DORGANIC%2526u%253Durn%25253Ali%25253Aactivity%25253A6179622770167648256&atv=2) **(P) Ltd – Kozhikode, India**

**Designation:** Quality Analyst Engineer (Sept 2014 – Feb 2017)

 **Job profile:-**

* Prepared test plans, wrote test cases and verified test cases documented by the QA team pertaining to specific features in the system.
* Testing software to identify and resolve problems from an end users perspective.
* In charge of testing developed software against specified requirements.
* Performed software testing to measure Key Performance Indicators to improve the performance of the applications
* Created and executed test cases for Software Policy Customization
* Preparing detailed test cases for the software.
* Responsible for designing and executing the Test cases on various MFP features
* Automated Testing using TEST COMPLETE tool.
* Requirement Management using IBM Rational Doors Next Generation
* Analyzed logs generated reports and identified failures.
* Identified bug trends and participated in general QA initiatives
* Responsible for sending progress reports on the weekly basis and resolving technical issues
* Responsible for gathering test execution data on daily basis and sending mail to Team
* Responsible for getting clarification from the Development Team regarding defects
* Regularly interacted and followed up with the development Team to discuss the discrepancies identified

Projects Under Taken

* **DESIGN AND SYNTHESIS OF RTC ON FPGA USING VHDL**

 A Real Time Clock was synthesized on Field Programmable Gate Array using VHDL language.

 Language : VHDL

 Duration : 2 Months

 Place : National Institute of Electronics and

 Information Technology, Calicut

Responsibility : Design, Analysis, Coding, Documentation

 Tools : ModelSim

* **WHISTLE RESPONDER**

It is a responder when a sound of certain frequency is applied as input to it. It was designed with the objective to help people find their lost objects just through a beep.

 Duration : 1 Month

Responsibility : Analysis, Coding

* **SEMINAR - DESIGN AND EVALUATION OF A MULTIUSER BCI VIDEO GAME BASED ON MOTOR IMAGERY**

The presentation mainly focuses on a Brain computer interface which is a latest and unavoidable technology coming up. The seminar also focuses about how efficiently BCI can be used among multiuser, by using an example of brain arena-which is a video game based on motor imagery

Academic Record

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Degree** | **Year** | **Institution** | **University / Board** | **%** |
| BTECH -EC | 2014 | AWHEC,CALICUT | University Of Calicut | 7.87/10 |
|  HSC |  2010 | SILVER HILLS HSS, | Board of Higher Secondary, Kerala |  92 |
|  10th |  2008 | HILLTOP PUBLIC SCHOOL, CALICUT | Central Board Of Secondary Education -Kozhikode | 86 |

Personal Profile

**Date of birth :** 28th Oct 1992

**Religion :** Islam-Muslim

**Sex :** Female

**Marital Status :** Married

**Nationality** **:** Indian

**Languages :** English, Hindi, Malayalam

**Strengths :** Dynamic Personality, Leadership Quality, Good communication Skills