**Mrs. KEERTHI**

**E-mail :** **keerthi.370600@2freemail.com**

**CAREER OBJECTIVE:**

To gain employment with electronics company that offers me a consistently positive atmosphere to learn new technologies and implement them for the betterment of the business and to use my skills in the best possible way for achieving the company’s goals

**ACADEMIC PROFILE:**



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| **Course** | **Year of** | **Board** | **Institution** | **Percentage of** |  |
|  | **Passing** | **marks** |  |
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| --- | --- | --- | --- | --- | --- | --- |
|  | M. Tech |  | Mahatma | SAINTGITS College Of |  |  |
|  | (VLSI & | 2016 | Gandhi | Engineering, Pathamuttom, | 7.74 (CGPA) |  |
| Embedded Systems) |  | University, | Kottayam |  |  |
|  |  |  |  | Kerala |  |  |  |
| B.Tech (Applied |  |  |  |  |  |
|  | Electronics |  | Mahatma | SAINTGITS College Of |  |  |
| and Instrumentation | 2014 | Gandhi | Engineering, Pathamuttom, | 7.47 |  |
|  | Engineering) |  | University, | Kottayam |  |  |
|  |  |  |  | Kerala |  |  |  |
|  |  |  |  |  | SACRED HEARTS HSS |  |  |
|  |  |  | 2009 | Kerala State | Changanacherry |  |  |
| Higher Secondary |  | 81.2 |  |
|  | Board |  |  |
|  |  |  |  |  |  |  |
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|  |  |  |  |  | SACRED HEARTS HSS |  |  |
|  |  |  | 2007 | Kerala State | Changanacherry |  |  |
|  | Secondary |  |  | 90 |  |
|  |  |  | Board |  |  |
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|  |  |  |  |  |  |  |
| **TECHNICAL SKILLS:** |  |  |  |  |  |
|  | Key Skills | : Device Modelling, CMOS Logic Design |  |  |
|  | Languages | : C, C++, Verilog, MATLAB , LabVIEW |  |  |
|  | EDA Tools | : Good knowledge in Silvaco Atlas TCAD |  |  |

* + Good knowledge in Visual TCAD
* Good knowledge in Mentor Graphics Bank end tools
	+ Good knowledge in Xilinx ISE 14.5

**ACHIEVEMENTS & AWARDS:**

* Core committee member of SFERICS 2013(National level techno-cultural fest organized by AEI DEPT.,SAINTGITS)
* Active member of National Service Scheme during years 2011 & 2013.
* Won the best Mini Project award for the topic “Industrial Station Monitoring using LABVIEW”**.**
* Student coordinator of **IEEE** sponsored **Conferences** on Solid State Circuits (**SSCS**) during 2015-2016.

**CONFERENCES, WORKSHOPS AND TRAININGS**

1. Present a paper on IEEE SSCS Sponsored 4th Conference on Solid State Circuits organized by SAINTGITS student chapter
2. Attended Faculty development programme on innovative research ideas in multi-disciplinary areas
3. Attended IETE Sponsored National level Faculty development programme on Embedded Design Using ARM & ARDUINO organized by Department of ECE in association with Pantech
4. Attended National level workshop on Signal processing in VLSI system design jointly organized by Department of ECE & KSCSTE in association with CoreEL

XILINX

1. Attended IEEE SSCS Sponsored Technical talk on VLSI Design Automation
2. Attended add on course on FPGA based system design and Verilog HDL simulation using MODELSIM
3. Attended add on course on Applications of PSPICE on network analysis and circuit

design.

1. ISTE workshop on Aakash Android Application Programming.
2. Attended a workshop on LABVIEW conducted by National Instruments.
3. Attended a workshop on Accelero-Botix,a robotics workshop conducted by Technophilia.
4. Attended 2nd International Conference on Emerging Trends in Technology and Applied Sciences **(ICETTAS) 2015.**

**STRENGTHS AND ATTRIBUTES**

* Quick grasping power
* Leadership quality
* Good team player
* Adaptability to new environment
* Communication skills
* Multilingual
* Soft skills
* Holding my calm in panic situations

**HOBBIES**

* Driving, music and movies.

**PROJECT PROFILE:**

 **M.TECH MAIN-PROJECT: A STUDY FOR PERFORMANCE ENHANCEMENT OF**

**SOLAR CELLS**

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This work presents a comparative study of Silicon and GaAs solar cell and also a latest technique Photon Enhanced Thermionic Emission with improved performance using ATLAS from SILVACO international.

 **M.TECH MINI-PROJECT: ANALYSIS OF TEMPERATURE DEPENDENT PARAMETERS ON SOLAR CELL EFFICIENCY**

This work analyses the temperature dependence on solar cells efficiency parameters like open ciecuit voltage, short circuit current and fillfactor using MATLAB

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* **B.TECH PROJECT: COMPUTER CONTROLLED INTRUSION-DETECTOR AND CONTROL ACTION UNIT FOR RESTRICTED AREA**

This work suggests a novel computer controlled intrusion detector and automatic firing unit which may be used for the surveillance of areas requiring higher security.

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|  |  **B.TECH MINI PROJECT: INDUSTRIAL STATION MONITORING USING LABVIEW** |  |
| This work monitors real time physical parameters like temperature, pressure based on LABVIEW . |  |
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|  | **TRAINING/COURCES ATTENTED:** |  |
|  | : **IN-PLANT TRAINING :** In-plant training at BDPP, Brahmapuram, Kochi. |  |
| : In-plant training at TCL, Kottayam. |  |
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|  | **PUBLICATIONS** |  |
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* **Keerthi** , Jitty Jose and Ajith Ravindran,”Analysis of temperature dependent parameters onsolar cell efficiency using MATLAB” in International Journal of engineering development and research (IJEDR), Vol 4, Issue 3, August 2016.
* **Keerthi** , Jitty Jose and Ajith Ravindran, " A Review on the Temperature DependentParameters on Solar Cell Efficiency using Atlas ", *in the Proceedings of 4th* *Conference on Solid State* *Circuits 2016,* pp.197-206
* **Keerthi** , Jitty Jose and Ajith Ravindran, " A Review on the Temperature DependentParameters on Solar Cell Efficiency using Atlas ", ICTACT Journal of microelectronics (Submitted).

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| **Personal particulars** |  |  |
| Name | Keerthi  |  |
| Gender | Female |  |
| Date of Birth | 07th March 1991 |  |
| Marital Status | Married |  |
| Languages Known | English, Malayalam, Hindi, |  |
| Telugu, Tamil |  |
|  |  |
| Visa Status | Spouse |  |

**DECLARATION:**

I hereby declare that the above furnished information is true to the best of my knowledge and belief and can be supported by relevant documents as and when required.

Place : Dubai

Date (Keerthi)