Vandana

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**EDUCATION**

# Master of Engineering (ME) – Electronics and Telecommunication – 79% (First

Year) ***Aug’18-Jun’20***

Fr.C.Rodrigues Institute of Technology (affiliated to the University of Mumbai), Navi Mumbai, India

# Bachelor of Engineering (BE) – Electronics and Telecommunication – 72.13%

## Aug’09-Jun’13

Pillai’s Institute of Technology (affiliated to the University of Mumbai), Navi Mumbai, India

**12th (PCMB)** - Maharashtra State Board- **88%**

## Apr’08-Apr‘09

**10th –** Maharashtra State Board **– 91.23% Apr’06-Mar’07**

**WORK EXPEREINCE (4 years and 8 months)**

# Assistant Engineer- (8th January 2015 to 31st July 2018).

* Worked closely with electronics department handling PLCs and cold storages**.**
* Worked as general admin.

# Assistant Network Engineer- (8th October 2014 to 29th November 2014).

* Responsible for analyzing the work of NOC department, making reports of NOC inputs of PAN India network and discussing the weekly performance of team with the manager.
* Worked closely with network engineers and project managers in ensuring smooth deliverables.

# Trainee Engineer – (3rd February 2014 to 28th August 2014).

Unified communication professional responsible for handling following technologies and products-

* Cisco Data: Routing and switching, Firewall and Security.
* Cisco Voice: IP Telephony (IPT), Cisco Unified Communication (CUCX) Systems, IPCC.
* Avaya Blue (Nortel): Ethernet Switching and Routing, IP Telephony (IPT), UC Systems, Avaya Aura Contact Center (AUCC) Systems.
* Avaya Red: Meridian/Succession/CS1000 Systems, VOIP fundamentals, IP Telephony

(IPT), Avaya Call Center (CC) introduction, Callpilot.

# Trainee- (7th July 2013 to 10th

**January 2014).**

* Internship activity included projects and coursework related to routing and switching.

**ACADEMIC PROJECTS**

# Implementation of Digital Filters using FPGA – (ME Project)

**July 2019 - April 2020.**

* Implementation of digital filters using FPGA as part of ME Final year Project Work. This project is being done at SAMEER (Society for Applied Microwave Electronics Engineering and Reach), IIT Bombay. It is being developed as a small part of Cancer Treatment machine developed at SAMEER recently.
* Here we are studying Digital Filters and FPGA in depth so as to make progress in this project.

# Implementation of data centre – (BE Group Project) July 2012 - April 2013.

* Implementation of data centre offering voice, video and data services using IPv4 and migrating to IPv6-final year project under the guidance of Indian Telecom training centre MTNL CETTM, MUMBAI.
* We established an intra network connection using dual stack routers and switch on IPv4 and then migrated to IPv6. Implemented and studied various servers like DNS, FTP, DHCP, MAIL, WEB, VOIP and routing protocols like OSPF, EIGRP.

# Liquid Level Sensor –(Third Year Engineering Group Project) July 2011 - Jan 2012.

* The aim was to monitor liquid level since level measurement is an integral part of process control and maybe used in a wide variety of industries.

# Multipattern running lights –(Third Year Engineering Group Project) July 2011 - Jan 2012.

* The aim was to achieve a circuit which contains lights running in multiple patterns making use of microcontroller IC 8051.

**ACHIEVEMENTS AND EXTRACURRICULAR ACTIVITIES**

* + Major Certification: Cleared CCNA Routing and Switching Written Exam (score: 986/1000). (7th October 2014).
  + Completed the workshop on “Computer Fundamentals and Assembling”

from Sunbeam Institute of Information Technology. (26th to 27th October 2013).

* + Completed the Course in “Converged Communications” from MTNL CETTM, Powai. (16th July 2012 to 21st July 2012)
  + Presented a paper at the Technical Paper Presentation Contest held at PIIT.

(18th October 2012).

* + Won second prize in the event “JUNKIE TECHIE” during college fest IMPULSE 2012.
  + Actively involved in organizing and executing the fest IMPLUSE 2012 at

college in the capacity of Volunteer (Publicity).

* + Won second prize in “Anveshan ‘13”, a national level project showcase. (4th April 2013).
  + Participated in “Avishkar ‘13”, a national level project competition. (25th

March 2013)

* + Presented a paper on “Implementation of data centre” at “National Conference on ‘Emerging Trends in Electronics Engineering & Technology’ (Nirman ’13)”. (22nd to 23rd February 2013).
  + Contributed in publishing “PIIT Journal of Electronics and Telecommunication” at college for the academic year 2011-2012